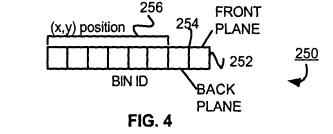
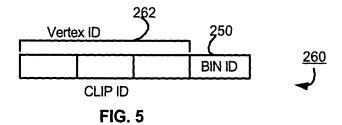
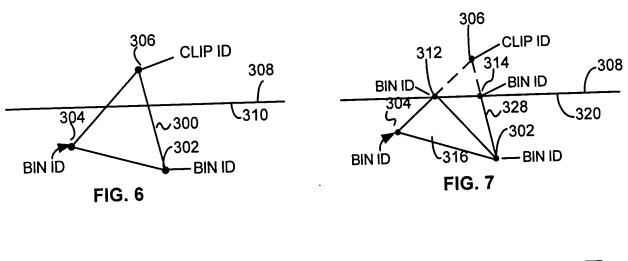


FIG. 3







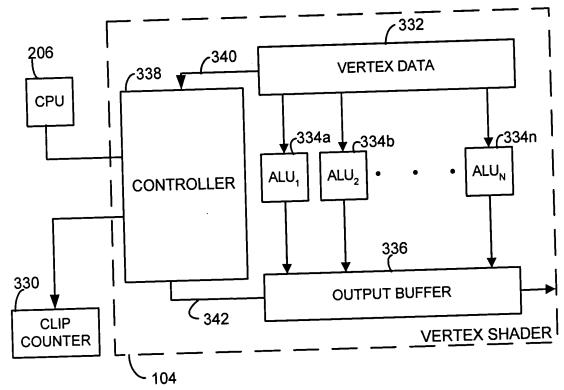


FIG. 8

Inventor: Munshi et al., Docket No. 00100.03.0007

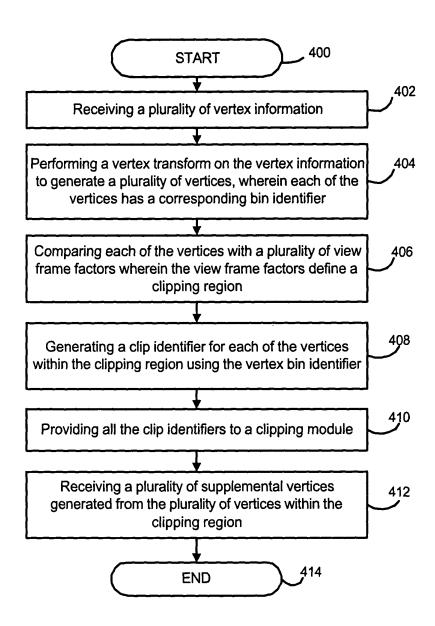


FIG. 9

420 **START** Receiving a plurality of vertex information; performing a vertex transform on the vertex information to generate a plurality of vertices, wherein each of the 422 vertices has a corresponding bin identifier Comparing each of the vertices with a plurality of view frame factors wherein the view frame factors define a clipping region Generating a clip identifier for each of the vertices within the clipping region using 426 the vertex bin identifier Incrementing a clip counter value each time a vertex is deemed within the 428 clipping region Providing the clip counter value to a central process unit such that the central processing unit knows the number of clip identifiers to be provided to the clipping 430 module Providing all the clip identifiers to the clipping module; and receiving a plurality of supplemental vertices generated from the plurality of vertices within the clipping **~** 432 region Performing a viewport transform on the plurality of vertices and the plurality of supplemental vertices; providing an output signal from the viewport transform to a setup engine Generating a setup engine output; and providign the setup engine output to a 436 rasterization engine Generating a rasterization engine output signal; providing the rasterization engine 438 output signal to a pixel operation module; transforming the rasterization engine output signal to generate a viewable output display signal; and providing the viewable output display signal to a frame buffer 440 Providing the viewable display signal from the frame buffer to a display device END FIG. 10

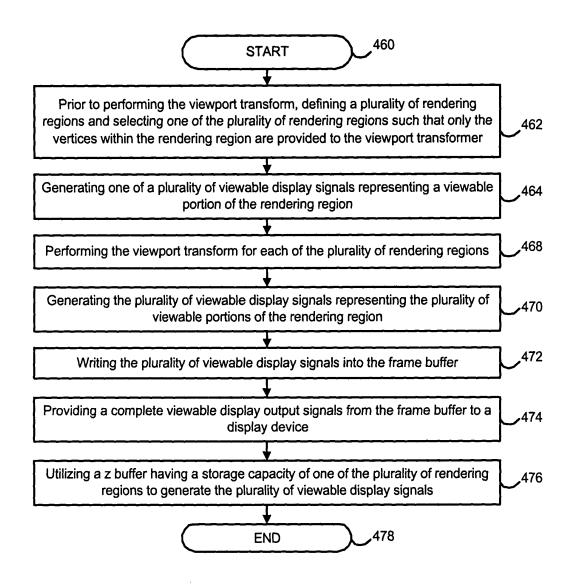


FIG. 11

Inventor: Munshi et al., Docket No. 00100.03.0007

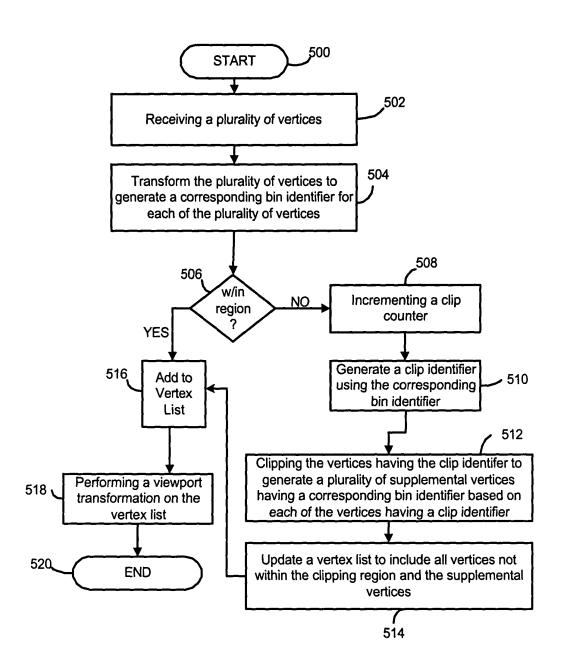


FIG. 12